

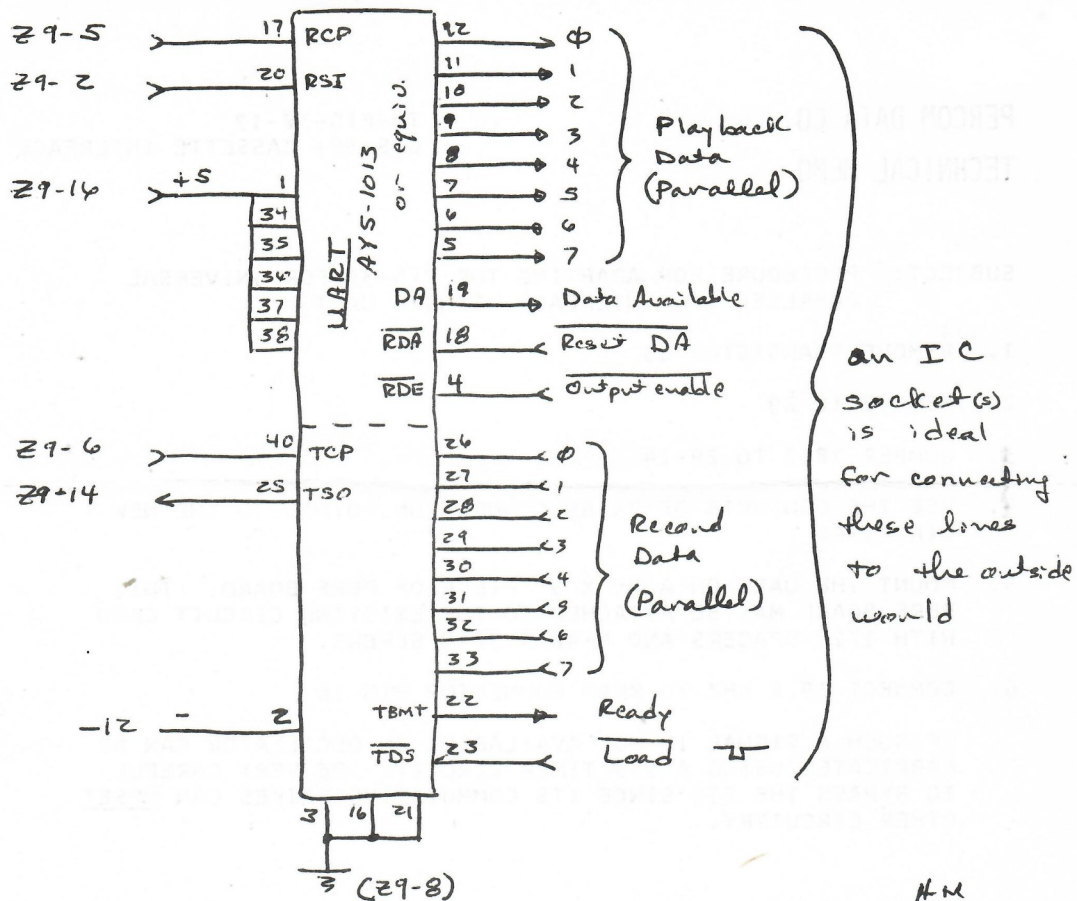
PERCOM DATA CO.
TECHNICAL MEMO

TM-CIS-30-12
CIS-30+ CASSETTE INTERFACE

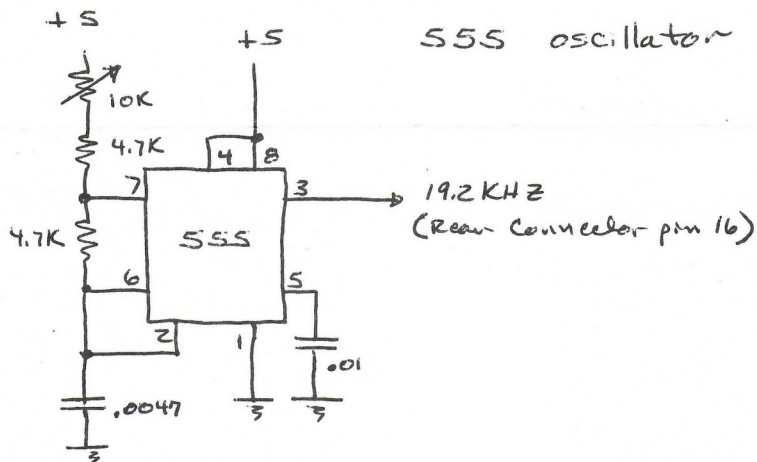
SUBJECT: PROCEDURE FOR ADAPTING THE CIS-30 FOR UNIVERSAL
PARALLEL I/O INTERFACE USING A UART

1. REMOVE TRANSISTOR Q3
2. REMOVE IC Z9
3. JUMPER Z8-2 TO Z9-14
4. USE THE CONTACTS OF Z9 AS CONNECTION POINTS TO THE NEW
CIRCUITRY
5. MOUNT THE UART ON A 4" X 5" PIECE OF PERF BOARD. THIS
PERF BOARD MAY BE ATTACHED TO THE EXISTING CIRCUIT CARD
WITH 1/2" SPACERS AND 4-40 X 3/4" SCREWS.
6. CONNECT 19.2 KHZ TO REAR CONNECTOR PIN 16

IF SUCH A SIGNAL IS NOT AVAILABLE, AN OSCILLATOR CAN BE
FABRICATED USING A 555 TIMER CIRCUIT. BE VERY CAREFUL
TO BYPASS THE 555 SINCE ITS COMMUTATING SPIKES CAN UPSET
OTHER CIRCUITRY.



HM
9-2-77



Bypass pins 1 to 8 with a 0.1 ufd Capacitor

NMM 9-2-77