```
00010                          NAM    CORES    PATCH
00020                          OPT    O
00030                  * WRITTEN BY H.A. MAUCH
00040                  * PERCOM DATA CO. 12/19/77
00050                  ************************************************
00060                  * THE FOLLOWING PATCHES MODIFY THE SWTP CORESIDENT
00070                  * EDITOR/ASSEMBLER TO PERMIT A STANDARD ASCII
00080                  * PRINTER TO BE USED INSTEAD OF THE SWTP PR-40.
00090                  * THIS PATCH ASSUMES AN MP-C INTERFACE IS USED FOR
00100                  * THE CONTROL PORT. AN MP-S INTERFACE WILL REQUIRE
00110                  * MODIFICATION. THE PATCH ASSUMES THE PRINTER WILL
00120                  * BE CONNECTED VIA AN MP-C INTERFACE IN THE COMPUTER
00130                  * PORT SLOT `O`.  IN MY SYSTEM I USE A DECWRITER(TM)
00140                  * FOR HARDCOPY OUTPUT.  THIS PATCH ALSO CLEANS UP TH
00150                  * SWTBUG(TM) OUTPUT ROUTINE SO THE PERCOM CIS-30
00160                  * WILL FUNCTION RELIABLY AT 1200 BAUD. THE PATCH
00170                  * MAY BE MODIFIED FOR MIKBUG BY CHANGING THE PRINTER
00180                  * PORT PIA INITIALIZATION AND THE REFERENCES (CALLS)
00190                  * TO `XTEMP`, `DEL`, AND `OUT1`.
00200                  ************************************************

00220                  * INITIALIZE PRINTER PORT
00230 17A2                     ORG    $17A2
00240 17A2 CE 8000             LDX    #$8000   POINT TO PRINTER PORT
00250 17A5 BD E284             JSR    PIAINI   SWTBUG PIA INITIALIZATION
00260 17A8 BD E27D             JSR    PIAECH   SWTBUG PIA INITIALIZATION
00270 17AB 01                  NOP
00280 17AC 01                  NOP


00300                  * OUTPUT ROUTINE
00310 1A9D                     ORG    $1A9D
00320 1A9D FF A010             STX    XTEMP    SAVE INDEX
00330 1AA0 CE 8000             LDX    #$8000   POINT TO PRINTER PORT
00340 1AA3 37                  PSH B           SAVE B
00350 1AA4 F6 01C8             LDA B  $1C8     GET TERM/PRINTER FLAG
00360 1AA7 7E 1A83             JMP    $1A83

00380 1A83                     ORG    $1A83
00390 1A83 27 03               BEQ    P1       BRANCH IF PRINTER
00400 1A85 CE 8004             LDX    #$8004   POINT TO CONTROL PORT
00410 1A88 C6 09   P1          LDA B  #9       SET UP BIT COUNTER
00420 1A8A BD E25A             JSR    DEL      BIT DELAY (FIXES SWTBUG'S PRO
00430 1A8D 6A 00               DEC    0,X
00440 1A8F 7E E248             JMP    OUT1     USE SWTBUG TO FINISH OUTPUT

00460                  ************************************************
00470                  * THE FOLLOWING PATCH ADDS AN APPEND FUNCTION
00480                  * TO THE CORES COMMANDS, APPEND BEHAVES THE
00490                  * SAME AS LOAD BUT LEAVES THE FILE BUFFER INTACT
00500                  * AND APPENDS NEW TEXT FROM TAPE TO THE END OF
00510                  * THE FILE BUFFER
00520                  ************************************************
```

```
00540 1A92 DE FA    APP     LDX     $FA       GET END OF FILE POINTER
00550 1A94 7E 19BB          JMP     $19BB     JUMP INTO LOAD ROUTINE

00570 1614                  ORG     $1614
00580 1614 41               FCC     /AP/ APPEND COMMAND
      1615 50
00590 1616 1A92             FDB     APP

00610                 ************************************************
00620                 * THIS PATCH REMOVES THE BACKSPACE ECHO WHEN A
00630                 * BACKSPACE CODE IS STRUCK.  THIS IS TO AVOID
00640                 * THE ANNOYING DOUBLE BACKSPACE WHEN USING A
00650                 * TERMINAL WITH THE BACKSPACE FUNCTION BUILT IN.
00660                 * FOR EXAMPLE - THE LSI ADM-3A
00670                 ************************************************
00680 1643                    ORG     $1643
00690 1643 09                 FCB     9

00710                 ************************************************
00720                 * THE FOLLOWING PATCHES MERELY PICKUP VARIOUS
00730                 * CHARACTER OUTPUT CALLS AND DIRECT THEM THROUGH
00740                 * THE IMPROVED OUTPUT ROUTINE DESCRIBED EARLIER.
00750                 ************************************************
00760 016F                    ORG     $016F
00770 016F 1A9D               FDB     $1A9D     NULL CODE OUTPUT
00780 0173                    ORG     $0173
00790 0173 1688               FDB     $1688     OBJECT CODE OUTPUT
00800 01FB                    ORG     $01FB
00810 01FB 1A9D               FDB     $1A9D     DELETE CODE OUTPUT

00830                 * MISC LINKAGES TO SWTBUG AND CORES
00840       E284      PIAINI  EQU     $E284     SWTBUG PIA INITIALIZATION
00850       E27D      PIAECH  EQU     $E27D     SWTBUG PIA INIIALIZATION
00860       A010      XTEMP   EQU     $A010     USED BY SWTBUG TO SAVE INDEX
00870       E25A      DEL     EQU     $E25A     SWTBUG BIT DELAY ROUTINE
00880       E248      OUT1    EQU     $E248     ENTRY POINT TO SWTBUG OUTPUT

00900                 * THIS ASSEMBLY WAS GENERATED USING CORES PATCHED AS
00910                 * DESCRIBED IN THIS LISTING.  THE SOURCE CODE WAS
00920                 * SAVED ON CASSETTE AT 1200 BAUD USING THE PERCOM
00930                 * CIS-30+.  THE LISTING WAS PRODUCED ON A DECWRITER
00940                 * CONNECTED TO PORT `O` VIA AN MP-C INTERFACE.
00950                       END

TOTAL ERRORS 00000

S00B0000434F524553205041C7
S10E17A2CE8000BDE284BDE27D0101A9
S1101A9DFFA010CE800037F601C87E1A832A
S1171A832703CE8004C609BDE25A6A007EE248DEFA7E19BBCB
S107161441501A9291
S10416430999
S105016F1A9DD3
S10501731688E8
S10501FB1A9D47
S9
TOTAL ERRORS 00000
```